How do I go to and return from a subret

- ANR Call Addressing Modes

Relative The relative address is encoded in the malphot instruction using 12 bits. Assuming that the Program Counter (PC) is pointing at the next Squrection to be secured, a relative call can jump within a range of 2.7° to 27° 1–1 program words in other words -2K s PC < 2K-1

Long Juli 16 K word (12K byte) address space indirect Juli 16 K word (12K byte) address space

- Why Subroutines?

- My Little Subroutine Dictionary

- Assembly Subroutine Template

- How to Send Information to and/or from the Calling Program

- Rules for Working with Subroutines

WHY SUBROUTINES? Code Organization – Gives the code organization and structure. A small step into the world or object-oriented programming. Modular and Hierarchical Design – Moves information about the program at the appropriate level of detail. Code Readability – Allows others to read products and the program in digestyble lites' instead of all at once. Higher level subroutines with many lower level subroutine calls the on the appearance of a high level language. Encapsulation – Insulates the rest of the program from changes made within a procedure. Team Development – Helps multiple programmers to work on the program in parallel: a first
step to configuration control. Allows a programmer to continue writing his code, independent
of other team members by introducing "stub" subroutines. A stub subroutine may be as simple
as the subroutine label followed by a return instruction.

Functions are very similar to subroutines; their syntax is nearly identical, and they can both performs to the code that called it. For this course the terms Sub- In everyday usage, "parameter" and "argume methods or functions. Cition. Parameter: void supervettee (uint8_t N) { ... } \leftarrow N is a para Argument: uint8_t X \times 10 mySubroutine (X) \leftarrow X is an argument

Subroutine:

push r15
in r15,SREG

push r16 mbly code dMySubroutine: clr r25 pop r16 out SREG,r15 pop r15 // zero-extended to 16-bits for C++ call (optional) // pop any flags or registers placed on the stack

ation to and from a subroutine or function. Here are a few. tting or clearing one of the bits in SREG (I, T, H, S, V, N, Z, C).

- In an SRAM variable, this method is not recommended.

- As part of a Stack Frame, this method is beyond the si

ROLES FOR WORKING WITH SUBROUTINES
to few rules to remember when writing your main program and subroutines.

Always disable interrupts and initiative the stack pointer at the beginning of your progr
to liable interrupts and configure stack pointer for 328P

Idi r16,low(RAMEND) // RAMEND address OxO8ff out SFL,r16 // Stack Pointer Low SFL at i/o address Ox3d Idi r16,high(RAMEND) out SFH,r16 // Stack Pointer High SFH at i/o address Ox3d

Pask ($push. z^2$) any registers modified by the subroutine at the beginning of the subroutine and pop ($pop. z^2$) in reverse order the registers at the end of the subroutine. This rule does not apply if you are using one of the registers or SREG flags to return a value to the calling program. Comments should clearly identify which registers are modified by the subroutine.

You cannot save the Status Register SREG directly onto the stack. Instead, first push one of the 32 registers on the stack and then save SREG in this register. Reverse the sequence at the end of the subroutine.

BASIC STRUCTURE OF A SUBROUTINE - A REVIEW 1. Load argument(s) into input registers (parameters) as specified in the header of the

3. Save an image of the calling programs CPU state by pushing all registers modified by the

4. Do something with the return value(s) stored in the output register(s) specified in the hi of the subroutine (typically r24, r22).

extern void mysubordint
extern uint0_t myFuno(u
In your Assembly Program...
/ Define Amerably Direc
DDEF parmil = r25
DDEF parmil = r25
DDEF parmil = r24
DDEF parmil = r24
DDEF parmil = r22
DDEF parmil = r21
DDEF parmil = r21
DDEF parmil = r20

Never jump into a subroutine. Use a call instruction {reall, call} to start executing code at the beginning of a subroutine.

Never (jump out of a subroutine. Your subroutine should contain a single return (ret) instruction as the last instruction (ret = last instruction).

You do not need an .ORG assembly directive. As long as the previous code segment ends correctly {rjnp, ret, ret.} your subroutine can start at the next address.

In your C Program...

// C Assembly Esternal Declarations
extern void symbol rights, t parant, uinti6_t para

All blocks of code within the subroutine or interrupt Service Routine (ISR) should exit the subroutine through the pop instructions and the return (ret, ret.).
It is a good programming practice to include only one return instruction (ret, ret.) located at the. It is a good programmi of the subroutine.

Once again, never jump into or out of a subroutine from the main program, an interrupt service routi
or any other subroutine. However, subroutines or ISRs may call (real1) other subroutines.

- Restore image of the calling programs CPU state by popping all registers modified by the subroutine, including loading SREG from a register.

subroutine, including saving SREG to a register.

push r15 in r15, SREG : out SREG, r15 pop r15

subroutine (typically r24, r22).

6. Return