PreReg0_20

Using the indirect addressing mode instruction st clear registers, and initialize registers r0 to r20 with values 0x15 to 0x01

Hint: SRAM address 0x0000 to 0x0001F map to registers r0 to r31

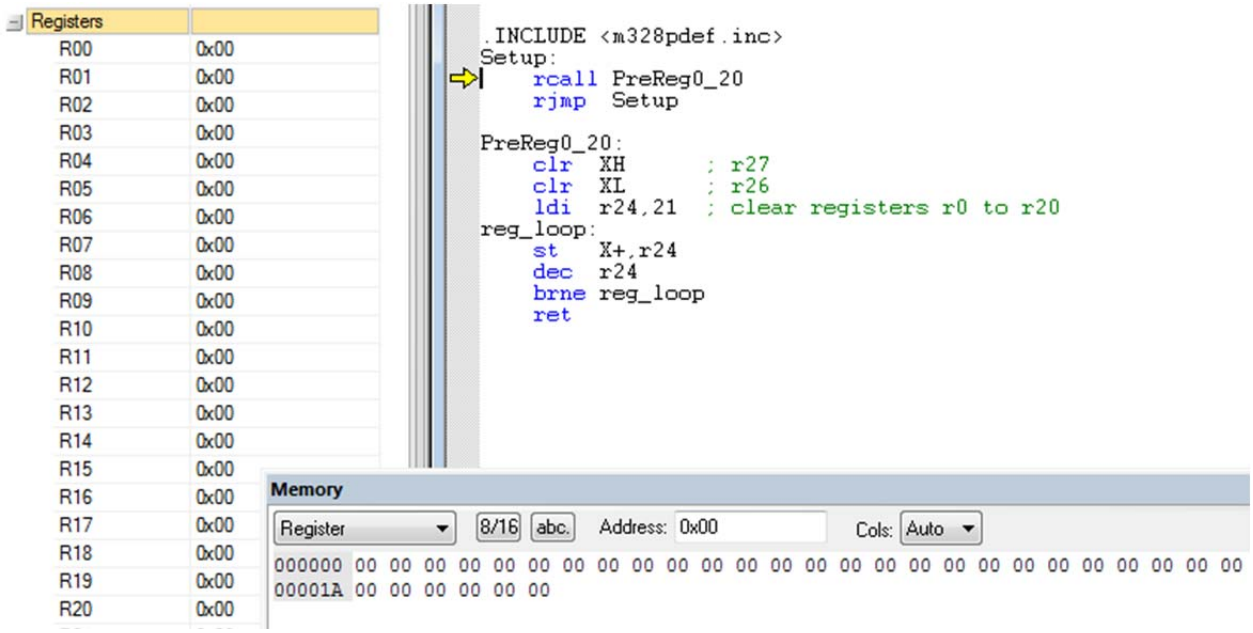

Figure 1 Start of assembly program to initialize registers r0 to r20. All registers cleared (reset condition).

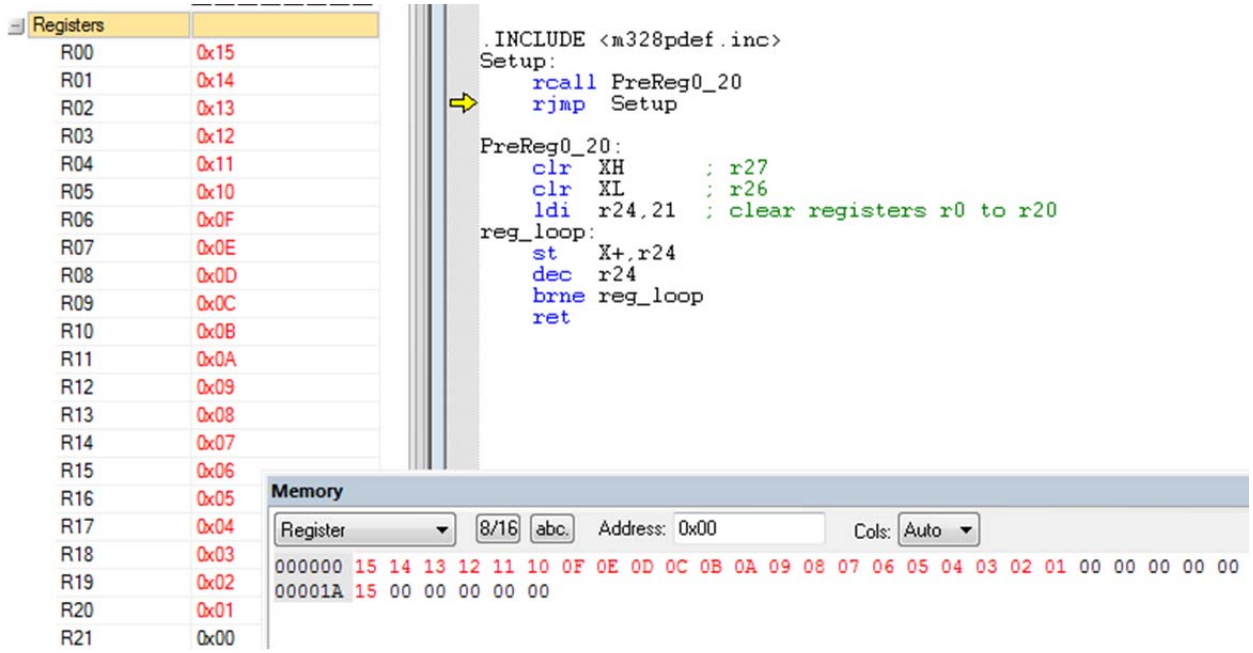

Figure 2 End of assembly program to initialize registers r0 to r20. Registers r0 to r20 set to values 0x15 to 0x01. Registers r21 to r31 are not modified (reset condition).