Pop15_0

Using the indirect addressing mode instruction ld and st, pop registers r15 to r0 from a stack buffer starting at SRAM address 0x4FF. To test your subroutine, begin by adding and then calling subroutines PreReg0_20, Push0_15, Clr0_15, Pop15_0 in the setup section of your code.

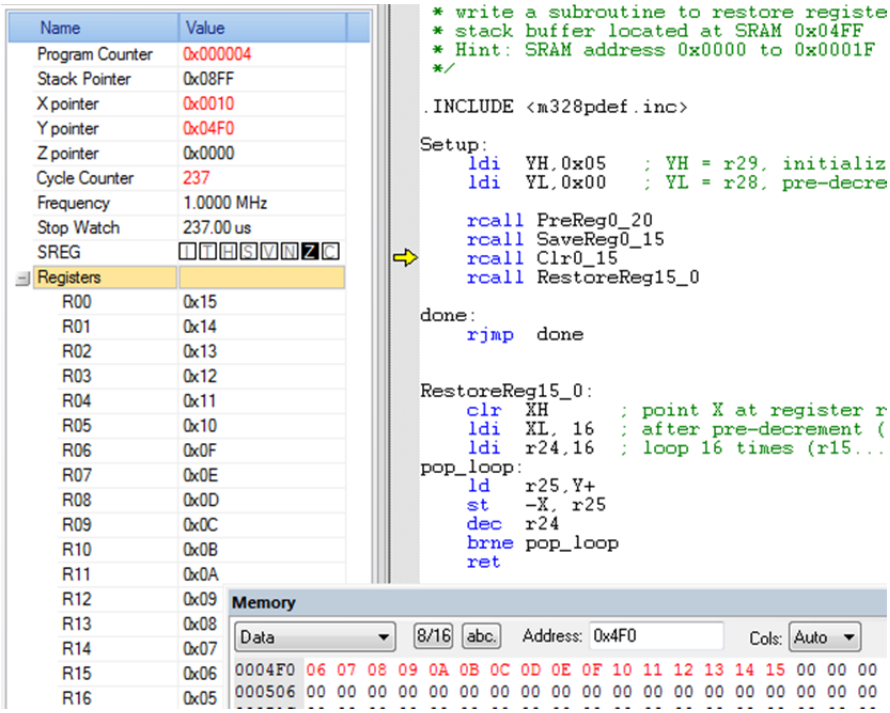

Figure 1 Registers r0 to r20 saved to stack buffer starting at location 0x04FF.

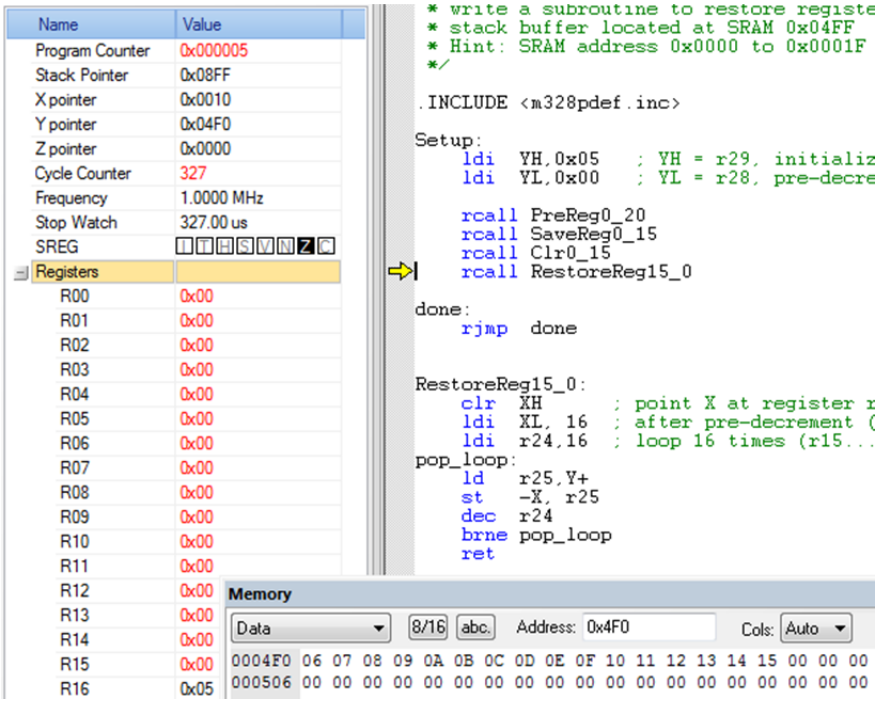

Figure 2 Registers r0 to r20 cleared.

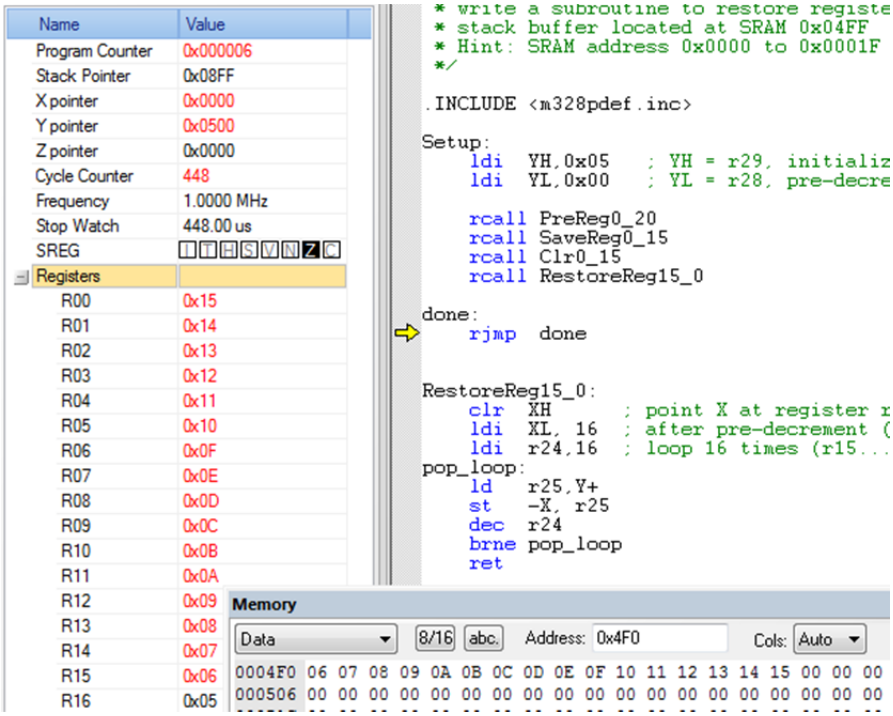

Figure 3 Registers r0 to r20 restored to saved values.