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; BlinkIt
; Version 1.0
; Date: 10/24/2014
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; -----

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BlinkIt: Simulation

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BlinkIt:
  lds r16,blink
  com r16
  sts blink,r16
  bst r16,0
  rcall TestIt
ret

TestIt:
  mov r16,spiLEDS
  sbr r16,0b10000000
  brts done
  cbr r16,0b10000000
done:
  mov spiLEDS,r16
  rcall WriteDisplay
//output to display

```

Name	Value	Type	Location
r16	0x00 ''	Register	R16
spiLED	0x00 ''	Register	R9
SREG	0x02 'γ'	I/O Register	0x003F [IO

Figure 1: Initialize blink to 0x00

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BlinkIt:
  lds r16,blink
  com r16
  sts blink,r16
  bst r16,0
  rcall TestIt
ret

TestIt:
  mov r16,spiLEDS
  sbr r16,0b10000000
  brts done
  cbr r16,0b10000000
done:
  mov spiLEDS,r16
  rcall WriteDisplay
//output to display

```

Name	Value	Type	Location
r16	0xFF 'ÿ'	Register	R16
spiLED	0x00 ''	Register	R9
SREG	0x55 'U'	I/O Register	0x003F [IO

Figure 2: End of BlinkIT blink is complement to 0xFF, T is set.