$\qquad$
Key $\qquad$
Frequency $\qquad$ Hz

Clock Frequency: $8 \mathrm{MHz}, 10 \mathrm{MHz}, 16 \mathrm{MHz}, 20 \mathrm{MHz}$
Timer: T0, T1, T2, Custom $\qquad$ (default 12 bits)

IMPOTANT: Please read before you begin:

1. Unless otherwise directed (see question 8), please provide base 10 answers using decimal notation (no powers of 2 or fractions $1 / 3$ ). If not a whole number, you can limit written answers to three places past the decimal point. Please round up or down as needed (round 5 down). For example 62.4875 would be written as 62.487 .
2. Prefix hexadecimal digits using $0 x$ notation.
3. To avoid round-off errors later in the quiz, use the memory function on your calculator to save the answer to earlier (dependent) question.
4. Always check your answers. When possible turn your answer into a question. For example, take your answer to question 9 and convert to a decimal number, the answer to question 8 , to verify that conversion is correct. Using this approach you can also verify your answers to questions 6 , 4 , and 7 .

|  | Question | Answer | Units |
| :--- | :--- | :--- | :--- |
| 1 | How many bits is the Timer/Counter | bits |  |
| 2 | Assuming the counter is cleared (equal to 0), How many tics of <br> the clock have occurred at the moment the overflow bit is set <br> (for example for Timer 1 overflow bit is TOV1)? | tics |  |
| 3 | What is the period of the waveform? |  | msec |
| 4 | Assuming a 50\% duty cycle, what delay should be programmed <br> into the Timer/Counter? | msec |  |
| 5 | What clock prescalar should be selected (1, 8, 64, 256, and <br> 1024)? |  | msec |
| 6 | Assuming this prescalar value, how many microseconds does it <br> take for 1 tic of the clock? | msec |  |
| 7 | What is the maximum delay that can be generated by this delay? | Base <br> 8 <br> What whole number (positive interger) should be preloaded <br> into the Timer/Counter register(s) to generate the desired delay <br> in decimal? <br> 9 <br> What value should be preloaded into the Timer/Counter <br> register(s) to generate the desired delay in hexadecimal? <br> 10What assemble instruction opcode mnemonic (do not include <br> operands) would you use to load this constant into register r16? | Base <br> 16 |
|  |  |  |  |

Show all your work here and on the back of this page.

